MALLA REDDY ENGINEERING COLLEGE FOR WOMEN



Autonomous Institution, UGC, Govt. of India

Programmes Accredited by NBA Accredited by NAAC with A+ Grade

Affiliated to JNTUH, Approved by AICTE, ISO 9001:2015 Certified Institute Maisammaguda (V), Dhullapally (Post), (Via) Kompally, Medchal Malkajgiri Dist. T.S-500100

B.TECH II YEAR (R20) I SEMESTER SUPPLY END EXAMINATIONS, NOV/DEC - 2024 TIME TABLE

TIME→ FN: 10.00 A.M TO 01.00 P.M

| BRANCH | 27-11-2024 FN WEDNESDAY | 29-11-2024 FN FRIDAY | 02-12-2024 FN MONDAY | 04-12-2024 FN WEDNESDAY | 06-12-2024 FN FRIDAY |
|---|-------------------------------|---|------------------------------|-------------------------------|--|
| ELECTRICAL AND ELECTRONICS ENGINEERING | Engineering Mechanics | Electrical Circuit Analysis | Mathematics-III | Analog Electronics | Python Programming |
| ELECTRONICS AND COMMUNICATION ENGINEERING | Mathematics-III | Electronic Devices & Circuits | Signals & Systems | Network Analysis | Python Programming |
| COMPUTER SCIENCE AND ENGINEERING | Probability & Statistics | Analog & Digital Electronic Circuits | Data Structures & Algorithms | Discrete Mathematics | Operating Systems |
| CSE - DS | Probability & Statistics | Analog & Digital Electronic Circuits | Data Structures & Algorithms | Discrete Mathematics | Object Oriented Programming Through JAVA |
| CSE – Al & ML | Probability & Statistics | Analog & Digital Electronic Circuits | Data Structures & Algorithms | Discrete Mathematics | Object Oriented Programming Through JAVA |
| CSE - IOT | Probability & Statistics | Analog & Digital Electronic Circuits | Data Structures & Algorithms | Discrete Mathematics | Computer Networks |
| CSE - CS | Probability & Statistics | Analog & Digital Electronic Circuits | Data Structures & Algorithms | Discrete Mathematics | Computer Networks |
| INFORMATION TECHNOLOGY | Probability & Statistics | Analog & Digital Electronic Circuits | Data Structures & Algorithms | Discrete Mathematics | Operating Systems |



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